IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

-Applicant:

Wendell P. Noble et al.

Examiner: Unknown

Serial No.:

Unknown

Group Art Unit: Unknown

Filed:

Herewith

9/55/027

Docket: 303.379US2

Title:

CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL

WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

PRELIMINARY AMENDMENT

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E. Willis 10-26-00

Box Patent Application Assistant Commissioner for Patents Washington, D.C. 20231

Before taking up the above identified application for examination, please amend it as follows:

IN THE SPECIFICATION

At the first line after the title, insert thereto: 'This application is a Divisional of U.S.

Application No. 08/939,742, filed October 6, 1997

IN THE CLAIMS

Please cancel claims 1-19. Claims 20-26 therefore remain pending in the application.

Applicant will file additional claims in a Supplemental Preliminary Amendment. The Examiner is invited to contact Applicants' representatives at the below-listed telephone number if examination begins before receiving the additional claims.

*Expr ss Mail" mailing label number: £154126133005 Respectfully submitted,

Dat of Deposit: _

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State of the state

April 17, 2000

I hereby certify that this paper or fee is being deposited with the United States Pestal Service "Express Mail Post Office to Addresses" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

WENDELL P. NOBLE ET AL.

By their Representatives,

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